

1. Record Nr.	UNINA9910144575403321
Titolo	Nonvolatile memory technologies with emphasis on Flash : a comprehensive guide to understanding and using NVM devices // edited by Joe E. Brewer, Manzur Gill
Pubbl/distr/stampa	Piscataway, New Jersey : , : IEEE Press, , c2008 [Piscataway, New Jersey] : , : IEEE Xplore, , 2008
ISBN	1-118-21162-6 1-281-22155-4 9786611221553 0-470-18135-4 0-470-18134-6
Descrizione fisica	1 online resource (787 p.)
Collana	IEEE Press series on microelectronic systems ; ; 8
Classificazione	ST 175
Altri autori (Persone)	BrewerJoe (Joe E.) GillManzur
Disciplina	004.5 006.786
Soggetti	Flash memories (Computers) Semiconductor storage devices
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	"IEEE Press, Wiley Interscience."
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Introduction to nonvolatile memory / Joe E. Brewer -- Flash memory applications / Gary Forni ... [et. al] -- Memory circuit technologies / Giulio G. Marotta, Giovanni Naso, Giuseppe Savarese -- Physics of flash memories / J. Van Houdt ... [et. al] -- Nor flash stacked and split-gate memory technology / Stephan N. Keeney, Manzur Gill, David Sweetman -- Nand flash memory technology / Koji Sakui, Kang-Deog Suh -- Dinor flash memory technology / Moriyoshi Nakashima, Natsuo Ajika -- P-channel flash memory technology / Frank Ruei-Ling Lin, Charles Ching-Hjsiang Hsu -- Embeddded flash memory / Chang-Kiang (Clinton) Kuo-Min Chang -- Tunnel dielectrics for scaled flash memory cells / T. P. Ma -- Flash memory reliability / Jian Justin Chen, Neal R. Mielke, Chenming Calvin Hu -- Multilevel cell digital memories / Albert Fazio, Mark Bauer -- Alternative memory technologies / Gary F. Derbenwick, Joe E. Brewer.

Sommario/riassunto

Presented here is an all-inclusive treatment of Flash technology, including Flash memory chips, Flash embedded in logic, binary cell Flash, and multilevel cell Flash. The book begins with a tutorial of elementary concepts to orient readers who are less familiar with the subject. Next, it covers all aspects and variations of Flash technology at a mature engineering level: basic device structures, principles of operation, related process technologies, circuit design, overall design tradeoffs, device testing, reliability, and applications.
