

1. Record Nr.	UNINA9910143556503321
Autore	Deschamps Jean-Pierre <1945->
Titolo	Synthesis of arithmetic circuits [[electronic resource]] : FPGA, ASIC and embedded systems / / Jean-Pierre Deschamps, Gery Jean Antoine Bioul, Gustavo D. Sutter
Pubbl/distr/stampa	Hoboken, N.J., : John Wiley, 2005
ISBN	1-280-41141-4 9786610411412 0-470-32398-1 0-471-74142-6 0-471-74141-8
Edizione	[1st edition]
Descrizione fisica	1 online resource (578 p.)
Altri autori (Persone)	BioulGery Jean Antoine SutterGustavo D
Disciplina	621.39/5 621.395
Soggetti	Computer arithmetic and logic units Digital electronics Embedded computer systems Electronic books.
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	SYNTHESIS OF ARITHMETIC CIRCUITS; CONTENTS; Preface; About the Authors; 1 Introduction; 1.1 Number Representation; 1.2 Algorithms; 1.3 Hardware Platforms; 1.4 Hardware-Software Partitioning; 1.5 Software Generation; 1.6 Synthesis; 1.7 A First Example; 1.7.1 Specification; 1.7.2 Number Representation; 1.7.3 Algorithms; 1.7.4 Hardware Platform; 1.7.5 Hardware-Software Partitioning; 1.7.6 Program Generation; 1.7.7 Synthesis; 1.7.8 Prototype; 1.8 Bibliography; 2 Mathematical Background; 2.1 Number Theory; 2.1.1 Basic Definitions; 2.1.2 Euclidean Algorithms; 2.1.3 Congruences; 2.2 Algebra 2.2.1 Groups 2.2.2 Rings 2.2.3 Fields 2.2.4 Polynomial Rings 2.2.5 Congruences of Polynomial 2.3 Function Approximation 2.4 Bibliography 3 Number Representation 3.1 Natural Numbers 3.1.1

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Sommario/riassunto

A new approach to the study of arithmetic circuits! In *Synthesis of Arithmetic Circuits: FPGA, ASIC and Embedded Systems*, the authors take a novel approach of presenting methods and examples for the synthesis of arithmetic circuits that better reflects the needs of today's computer system designers and engineers. Unlike other publications that limit discussion to arithmetic units for general-purpose computers, this text features a practical focus on embedded systems. Following an introductory chapter, the publication is divided into two parts. The first part, *Mathematical Aspects*
