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3.10.4. Optimum Noise Match; 3.10.5. Constant Noise Figure and Gain Circles; 3.10.6. Simultaneous Input and Noise Match
3.11. Dynamic Range
3.12. Multistage Amplifier Characteristics; 3.12.1. Multistage IP₃; 3.12.2. Multistage PAE; 3.12.3. Multistage NF; 3.13. Gate and Drain Pushing Factors; 3.14. Amplifier Temperature Coefficient; 3.15. Mean Time to Failure; References; Problems; 4. Transistors; 4.1. Transistor Types; 4.2. Silicon Bipolar Transistor; 4.2.1. Figure of Merit; 4.2.2. High-Frequency Noise Performance of Silicon BJT; 4.2.3. Power Performance; 4.3. GaAs MESFET; 4.3.1. Small-Signal Equivalent Circuit; 4.3.2. Figure of Merit; 4.3.3. High-Frequency Noise Properties of MESFETs
4.4. Heterojunction Field Effect Transistor
4.4.1. High-Frequency Noise Properties of HEMTs; 4.4.2. Indium Phosphide pHEMTs; 4.5. Heterojunction Bipolar Transistors; 4.5.1. High-Frequency Noise Properties of HBTs; 4.5.2. SiGe Heterojunction Bipolar Transistors; 4.6. MOSFET; References; Problems; 5. Transistor Models; 5.1. Transistor Model Types; 5.1.1. Physics/Electromagnetic Theory Based Models; 5.1.2. Analytical or Hybrid Models; 5.1.3. Measurement Based Models; 5.2. MESFET Models; 5.2.1. Linear Models; 5.2.2. Nonlinear Models; 5.3. pHEMT Models; 5.3.1. Linear Models; 5.3.2. Nonlinear Models
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6.4. Bond Wire Inductors

Sommario/riassunto

A Comprehensive and Up-to-Date Treatment of RF and Microwave Transistor Amplifiers This book provides state-of-the-art coverage of RF and microwave transistor amplifiers, including low-noise, narrowband, broadband, linear, high-power, high-efficiency, and high-voltage. Topics covered include modeling, analysis, design, packaging, and thermal and fabrication considerations. Through a unique integration of theory and practice, readers will learn to solve amplifier-related design problems ranging from matching networks to biasing and stability. More than 240 problems are included to help read
