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## Sommario/riassunto

State-of-the-art and beyond technologies to be used in future multi-mode wireless communication systems Current and future mobile terminals become increasingly complex because they have to deal with a variety of frequency bands and communication standards. Achieving multiband/multimode functionality (3G and beyond) is especially challenging for the RF-transceiver section. This volume presents cutting-edge physical layer technologies for multi-mode wireless RF transceivers, specifically RF, analog, and mixed-signal and digital circuits and architectures. Providing the most comprehensive treatment of this topic available, it features original contributions from distinguished researchers and professionals from both academia and industry, who anticipate the major trends and needs of future wireless system developments. Divided into four sections, Multi-Mode/Multi-Band RF Transceivers for Wireless Communications covers: Transceiver concepts and design: software-defined radio front-ends/transceivers, adaptive multi-mode RF front-end circuits, delay alignment between amplitude and phase paths in a digital polar transmitter, and front-end RF passive integration, as well as versatile data converters. Receiver design: OFDM transform-domain receivers for multi-standards, discrete-time processing of RF signals, oversampled ADC using VCO-based quantizers, RF receiver front-ends for mobile terminals, and digitally enhanced alternate path linearization of RF receivers. Transmitter techniques: Linearity and efficiency strategies, CMOS RF power amplifiers for mobiles, and digitally assisted RF architectures. Digital Signal Processing for RF transceivers: RF impairment compensation for future radio systems, techniques for the analysis of digital bang-bang PLLs, and low-power spectrum processors for cognitive radios The remarkable insight into the essential transceiver building blocks to be used in future multi-mode wireless communication systems makes this an invaluable resource for

engineers and researchers from academia and industry working on circuits and architectures of wireless transceivers, as well as for RF design engineers in semiconductor companies and graduate students taking advanced courses on wireless communication circuits.

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