

1. Record Nr.	UNISALENT0991002116309707536
Autore	Claudel, Paul
Titolo	Cent phrases pour éventails / Paul Claudel
Pubbl/distr/stampa	Paris : Gallimard, 1942
Descrizione fisica	[135] p. ; 19 cm
Altri autori (Persone)	Arishima, Ikuma
Disciplina	842.912
Lingua di pubblicazione	Francese
Formato	Materiale a stampa
Livello bibliografico	Monografia
2. Record Nr.	UNINA9910138914903321
Titolo	10th International Conference on Thermal, Mechanical and Multi-Physics Simulation and Experiments in Microelectronics and Microsystems : 26-29 April 2009, Delft, Netherlands / / Institute of Electrical and Electronics Engineers
Pubbl/distr/stampa	Piscataway, New Jersey : , : Institute of Electrical and Electronics Engineers, , 2009
ISBN	1-4244-4161-7
Descrizione fisica	1 online resource (114 pages)
Disciplina	621.3815
Soggetti	Integrated circuits - Simulation methods Microelectronics - Design Microelectronics - Simulation methods
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
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3. Record Nr.	UNINA9910146248503321
Autore	Rohde Ulrich L
Titolo	Microwave and wireless synthesizers : theory and design / / Ulrich L. Rohde
Pubbl/distr/stampa	New York, : Wiley, c1997
ISBN	9786610556427 9781280556425 1280556420 9780470358351 0470358351 9780471224310 0471224316
Edizione	[1st edition]
Descrizione fisica	1 online resource (xvii, 638 p. ) : ill. ;
Disciplina	621.3815/486
Soggetti	Frequency synthesizers - Design and construction Phase-locked loops Digital electronics Microwave circuits - Design and construction Radio frequency Phase-locked loops - Design and construction Microwave circuits Electrical & Computer Engineering Engineering & Applied Sciences Electrical Engineering
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	"A Wiley-Interscience publication."
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Loop fundamentals -- Noise and spurious response of loops -- Special loops -- Loop components -- Digital PLL synthesizers -- High-performance hybrid synthesizer.
Sommario/riassunto	Over the past decade, great strides have been made in the technology of microwave oscillators and synthesizers, with digital frequency synthesizers in particular attracting much attention. These synthesizers

are now being used in virtually all modern signal generators and radio communication equipment. Until now, however, detailed information about their design has been hard to come by-much of it scattered through journal articles-and most books on the subject have taken a primarily theoretical approach. Enter Microwave and Wireless Synthesizers-the first book to emphasize both practical circuit information from RF to millimeter-wave frequencies and up-to-date theory. Based on course material taught by author Ulrich L. Rohde at George Washington University and recent work done by the author at Compact Software, Inc. and Synergy Microwave Corporation, this volume is a complete revision and update of Rohde's landmark text, *Digital PLL Frequency Synthesizers: Theory and Design*. While it provides all the necessary theory and formulas, it also offers an in-depth look at the practical side of the phase-lock loop (PLL) in synthesizers-including special loops, loop components, and practical circuits-material that is not available in any other book. Rohde explains loop fundamentals, demonstrates the linear approach to oscillator phase noise, discusses the digital direct synthesizer technique, addresses low noise oscillator design, and provides insight into the role and design of crystal oscillators, mixers, phase/frequency discriminators, wideband high-gain amplifiers, programmable dividers, and loop filters. He goes on to cover conventional multiloop synthesizers and survey existing state-of-the-art microwave synthesizer applications. Extensive appendices review the mathematics of useful functions and various applications, including even the complex nonlinear theory of noise in large signal systems such as mixers and oscillators. *Microwave and Wireless Synthesizers* allows anyone with a PC running either Windows 3.11 or Windows NT to explore real-world design. It uses programs for the solution of digital phase-lock loop systems, tabulates the results, and shows how Bode diagrams are determined by the computer's graphic capabilities. It also includes examples using commercially available linear and nonlinear CAD programs to provide accurate evaluation and optimization of oscillators and other useful circuits and many practical charts. For companies invo...

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