1. Record Nr. UNINA9910135759703321

Titolo	ANSI/IEEE Std C37.26.1972 / / Institute of Electrical and Electronics Engineers
Pubbl/distr/stampa	Piscataway, NJ : , : IEEE, , 1972
ISBN	0-7381-4016-3 0-7381-1061-2
Descrizione fisica	1 online resource (12 pages)
Disciplina	621.3192
Soggetti	Electric circuits
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Sommario/riassunto	Methods used to measure the power factor in low-voltage test circuits are covered. Since the power factor measurement for high-capacity test circuits is particularly difficult, and different methods may yield different results, the methods that are least likely to yield errors are recommended for particular circuit conditions. The ratio method is recommended for fast clearing devices that may have total interruption times of 0.5 cycle or less. The DC decrement method is recommended for circuits with a 30% power factor or less when the device to be tested interrupts at a point in time more than 0.5 cycle from the initiation of the current. The phase relationship method, using current and voltage waves, is recommended for circuits having power factors over 30%.