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4.2.4 Dual Diode-Resistor-Grounded-Gate MOSFET

Sommario/riassunto

A comprehensive and in-depth review of analog circuit layout, schematic architecture, device, power network and ESD design. This book will provide a balanced overview of analog circuit design layout, analog circuit schematic development, architecture of chips, and ESD design. It will start at an introductory level and will bring the reader right up to the state-of-the-art. Two critical design aspects for analog and power integrated circuits are combined. The first design aspect covers analog circuit design techniques to achieve the desired circuit performance. The second and main aspect pres
