1. Record Nr. UNINA990006513410403321

Autore	Hargreaves, Roger
Titolo	Arguingand discussing / Roger Hargreaves , Mark Fletcher
Pubbl/distr/stampa	London : Evans, 1981
Descrizione fisica	70 p. ; 21 cm
Collana	Evans functional units
Disciplina	428
Locazione	FSPBC
Collocazione	XV IB 155
Lingua di pubblicazione	Italiano
Formato	Materiale a stampa
Livello bibliografico	Monografia

2. Record Nr. UNINA9910452431203321

Autore	Yiu Joseph
Titolo	The definitive guide to ARM Cortex-M3 and Cortex-M4 processors / / Joseph Yiu, ARM Ltd., Cambridge, UK
Pubbl/distr/stampa	Amsterdam : , : Newnes/Elsevier, , [2014] ©2014
ISBN	0-12-407918-0
Edizione	[Third edition.]
Descrizione fisica	1 online resource (1055 p.)
Disciplina	1055
Soggetti	Embedded computer systems Microprocessors Electronic books.
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Front Cover; The Definitive Guide to ARM® Cortex®-M3 and Cortex-M4

Processors; Copyright; Contents; Foreword; Preface; Synopsis; About this Book; Contributor Bio-Paul Beckmann; Acknowledgments; Terms and Abbreviations; Conventions; Chapter 1 - Introduction to ARM® Cortex®-M Processors; 1.1 What are the ARM® Cortex®-M processors?; 1.2 Advantages of the Cortex®-M processors; 1.3 Applications of the ARM® Cortex®-M processors; 1.4 Resources for using ARM® processors and ARM microcontrollers; 1.5 Background and history; Chapter 2 - Introduction to Embedded Software Development
2.1 What are inside typical ARM® microcontrollers?2.2 What you need to start; 2.3 Software development flow; 2.4 Compiling your applications; 2.5 Software flow; 2.6 Data types in C programming; 2.7 Inputs, outputs, and peripherals accesses; 2.8 Microcontroller interfaces; 2.9 The Cortex® microcontroller software interface standard (CMSIS); Chapter 3 - Technical Overview; 3.1 General information about the Cortex®-M3 and Cortex-M4 processors; 3.2 Features of the Cortex®-M3 and Cortex-M4 processors; Chapter 4 - Architecture; 4.1 Introduction to the architecture; 4.2 Programmer's model
4.3 Behavior of the application program status register (APSR)4.4 Memory system; 4.5 Exceptions and interrupts; 4.6 System control block (SCB); 4.7 Debug; 4.8 Reset and reset sequence; Chapter 5 - Instruction Set; 5.1 Background to the instruction set in ARM® Cortex®-M processors; 5.2 Comparison of the instruction set in ARM® Cortex®-M processors; 5.3 Understanding the assembly language syntax; 5.4 Use of a suffix in instructions; 5.5 Unified assembly language (UAL); 5.6 Instruction set; 5.7 Cortex®-M4-specific instructions; 5.8 Barrel shifter
5.9 Accessing special instructions and special registers in programmingChapter 6 - Memory System; 6.1 Overview of memory system features; 6.2 Memory map; 6.3 Connecting the processor to memory and peripherals; 6.4 Memory requirements; 6.5 Memory endianness; 6.6 Data alignment and unaligned data access support; 6.7 Bit-band operations; 6.8 Default memory access permissions; 6.9 Memory access attributes; 6.10 Exclusive accesses; 6.11 Memory barriers; 6.12 Memory system in a microcontroller; Chapter 7 - Exceptions and Interrupts; 7.1 Overview of exceptions and interrupts; 7.2 Exception types
7.3 Overview of interrupt management7.4 Definitions of priority; 7.5 Vector table and vector table relocation; 7.6 Interrupt inputs and pending behaviors; 7.7 Exception sequence overview; 7.8 Details of NVIC registers for interrupt control; 7.9 Details of SCB registers for exception and interrupt control; 7.10 Details of special registers for exception or interrupt masking; 7.11 Example procedures in setting up interrupts; 7.12 Software interrupts; 7.13 Tips and hints; Chapter 8 - Exception Handling in Detail; 8.1 Introduction; 8.2 Exception sequences
8.3 Interrupt latency and exception handling optimization

Sommario/riassunto

This new edition has been fully revised and updated to include extensive information on the ARM Cortex-M4 processor, providing a complete up-to-date guide to both Cortex-M3 and Cortex-M4 processors, and which enables migration from various processor architectures to the exciting world of the Cortex-M3 and M4. Key Features include: Two new chapters on DSP features and CMSIS-DSP software libraries, covering DSP fundamentals and how to write DSP software for the Cortex-M4 processor, including examples of using the CMSIS-DSP library, as well as useful information

3.	Record Nr.	UNISA996391314703316
	Titolo	The doctrine of the Bible, or, Rules of discipline [[electronic resource]] : brieflie gethered through the whole course of the scripture, by way of questions and answers
	Pubbl/distr/stampa	At London, : Printed by Richard Braddocke, for Thomas Pauier, and are to be sold at his shop, at the entering in of the Exchange, 1606
	Descrizione fisica	[4], 136, [2] p
	Soggetti	Questions and answers.
	Lingua di pubblicazione	Inglese
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
	Note generali	Reproduction of original in: Yale University Library.
	Sommario/riassunto	eebo-0198
4.	Record Nr.	UNINA9910786704403321
	Autore	Call Steve <1956->
	Titolo	Danger close [[electronic resource]] : tactical air controllers in Afghanistan and Iraq / / Steve Call
	Pubbl/distr/stampa	College Station, Tex., : Texas A&M University Press, c2007
	ISBN	1-60344-304-5
	Edizione	[1st ed.]
	Descrizione fisica	1 online resource (274 p.)
	Collana	Texas A & M University military history series ; ; no. 113
	Disciplina	956.7044/348
	Soggetti	Afghan War, 2001-2021 - Aerial operations, American Iraq War, 2003-2011 - Aerial operations, American Close air support - History - 21st century
	Lingua di pubblicazione	Inglese
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
	Note generali	Includes index.
	Nota di contenuto	List of illustrations -- Preface -- List of Air Force ranks and

abbreviations -- Prologue -- Part 1. Afghanistan -- The challenge is clear and daunting -- Integrating the Special Forces-close air support team -- The fall of the Taliban regime -- Operation Anaconda -- Just another day in Afghanistan -- Part 2. Iraq -- A controversial invasion in a context of controversy -- "Our business now is north" -- A tale of two bridges -- Through the gap, across the bridge, and on to Baghdad -- The thunder runs -- The Scud hunt and operations in western Iraq -- The drive from the north -- Conclusion -- Appendix : people interviewed -- Glossary -- Index.

Sommario/riassunto

"America had a secret weapon," writes Steve Call of the period immediately following September 11, 2001, as planners contemplated the invasion of Afghanistan. This weapon consisted of small teams of Special Forces operatives trained in close air support (CAS) who, in cooperation with the loose federation of Afghan rebels opposed to the Taliban regime, soon began achieving impressive-and unexpected-military victories over Taliban forces and the al-Qaeda terrorists they had sponsored. The astounding success of CAS tactics coupled with ground operations in Afghanistan soon drew the attention