

1.	Record Nr.	UNINA990004337320403321
	Titolo	CAUDILLO and peasant in the Mexican revolution / edited by D. A. Brading
	Pubbl/distr/stampa	Cambridge, London : Cambridge University press, 1980
	Descrizione fisica	XI, 311 p. ; 22 cm
	Collana	Cambridge Latin American studies ; 38
	Disciplina	972.0816
	Locazione	FLFBC
	Collocazione	972.08 BRA 1
	Lingua di pubblicazione	Italiano
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
2.	Record Nr.	UNINA9910782561703321
	Autore	Colman Tyler
	Titolo	Wine politics [[electronic resource]] : how governments, environmentalists, mobsters, and critics influence the wines we drink / Tyler Colman
	Pubbl/distr/stampa	Berkeley, : University of California Press, c2008
	ISBN	1-281-75268-1 1-4356-8478-8 9786611752682 0-520-93473-3
	Descrizione fisica	1 online resource (206 p.)
	Disciplina	338.4/76632
	Soggetti	Wine - Political aspects Wine industry
	Lingua di pubblicazione	Inglese
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
	Note generali	Description based upon print version of record.
	Nota di bibliografia	Includes bibliographical references (p. 167-172) and index.

Nota di contenuto	What is wine politics? -- Soil and society -- Authenticating origins -- Baptists and bootleggers -- Who controls your palate? -- Greens, grapes, and gripes -- Celebrating diversity.
Sommario/riassunto	After reading this intriguing book, a glass of wine will be more than hints of blackberries or truffles on the palate. Written by the author of the popular, award-winning website DrVino.com, Wine Politics exposes a little-known but extremely influential aspect of the wine business-the politics behind it. Tyler Colman systematically explains how politics affects what we can buy, how much it costs, how it tastes, what appears on labels, and more. He offers an insightful comparative view of wine-making in Napa and Bordeaux, tracing the different paths American and French wines take as they travel from vineyard to dining room table. Colman also explores globalization in the wine business and illuminates the role of behind-the-scenes players such as governments, distributors, and prominent critics who wield enormous clout. Throughout, Wine Politics reveals just how deeply politics matters- right down to the taste of the wine in your glass tonight.

3. Record Nr.	UNINA9910144341103321
Titolo	Automated Technology for Verification and Analysis : Second International Conference, ATVA 2004, Taipei, Taiwan, ROC, October 31 - November 3, 2004. Proceedings / / edited by Farn Wang
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2004
ISBN	3-540-30476-2
Edizione	[1st ed. 2004.]
Descrizione fisica	1 online resource (XII, 510 p.)
Collana	Lecture Notes in Computer Science, , 0302-9743 ; ; 3299
Disciplina	004.015113
Soggetti	Computer-aided engineering Computer logic Computers Computer networks Computers, Special purpose Software engineering Computer-Aided Engineering (CAD, CAE) and Design Logics and Meanings of Programs Information Systems and Communication Service Computer Communication Networks Special Purpose and Application-Based Systems Software Engineering

Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references at the end of each chapters and index.
Nota di contenuto	<p>Keynote Speech -- Games for Formal Design and Verification of Reactive Systems -- Evolution of Model Checking into the EDA Industry -- Abstraction Refinement -- Invited Speech -- Tools for Automated Verification of Web Services -- Theorem Proving Languages for Verification -- An Automated Rigorous Review Method for Verifying and Validating Formal Specifications -- Papers -- Toward Unbounded Model Checking for Region Automata -- Search Space Partition and Case Basis Exploration for Reducing Model Checking Complexity -- Synthesising Attacks on Cryptographic Protocols -- Büchi Complementmentation Made Tighter -- SAT-Based Verification of Safe Petri Nets -- Disjunctive Invariants for Numerical Systems -- Validity Checking for Quantifier-Free First-Order Logic with Equality Using Substitution of Boolean Formulas -- Fair Testing Revisited: A Process-Algebraic Characterisation of Conflicts -- Exploiting Symmetries for Testing Equivalence in the Spi Calculus -- Using Block-Local Atomicity to Detect Stale-Value Concurrency Errors -- Abstraction-Based Model Checking Using Heuristical Refinement -- A Global Timed Bisimulation Preserving Abstraction for Parametric Time-Interval Automata -- Design and Evaluation of a Symbolic and Abstraction-Based Model Checker -- Component-Wise Instruction-Cache Behavior Prediction -- Validating the Translation of an Industrial Optimizing Compiler -- Composition of Accelerations to Verify Infinite Heterogeneous Systems -- Hybrid System Verification Is Not a Sinecure -- Providing Automated Verification in HOL Using MDGs -- Specification, Abduction, and Proof -- Introducing Structural Dynamic Changes in Petri Nets: Marked-Controlled Reconfigurable Nets -- Typeness for ω-Regular Automata -- Partial Order Reduction for Detecting Safety and Timing Failures of Timed Circuits -- Mutation Coverage Estimation for Model Checking -- Modular Model Checking of Software Specifications with Simultaneous Environment Generation -- Rabin Tree and Its Application to Group Key Distribution -- Using Overlay Networks to Improve VoIP Reliability -- Integrity-Enhanced Verification Scheme for Software-Intensive Organizations -- RCGES: Retargetable Code Generation for Embedded Systems -- Verification of Analog and Mixed-Signal Circuits Using Timed Hybrid Petri Nets -- First-Order LTL Model Checking Using MDGs -- Localizing Errors in Counterexample with Iteratively Witness Searching -- Verification of WCDMA Protocols and Implementation -- Efficient Representation of Algebraic Expressions -- Development of RTOS for PLC Using Formal Methods -- Reducing Parametric Automata: A Multimedia Protocol Service Case Study -- Synthesis of State Feedback Controllers for Parameterized Discrete Event Systems -- Solving Box-Pushing Games via Model Checking with Optimizations -- CLP Based Static Property Checking -- A Temporal Assertion Extension to Verilog.</p>
Sommario/riassunto	<p>It was our great pleasure to hold the 2nd International Symposium on Automated Technology on Verification and Analysis (ATVA) in Taipei, Taiwan, ROC, October 31– November 3, 2004.</p> <p>This series of ATVA meetings is intended for the promotion of related research in eastern Asia. In the last decade, automated technology on verification has become the new strength in industry and brought forward various</p>

hot research activities in both Europe and USA. In comparison, easternAsia has been quiet in the forum. With more and more IC design houses moving from SiliconValley to easternAsia, we believe this is a good time to start cultivating related research activities in the region. The emphasis of the ATVA workshop series is on various mechanical and informative techniques, which can give engineers valuable feedback to fast converge their designs according to the specifications. The scope of interest contains the following research areas: model-checking theory, theorem-proving theory, state-space reduction techniques, languages in automated verification, parametric analysis, optimization, formal performance analysis, real-time systems, embedded systems, infinite-state systems, Petri nets, UML, synthesis, tools, and practice in industry.
