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| 1. Record Nr.           | UNINA990002865020403321                                                     |
| Autore                  | Chen, Lin                                                                   |
| Titolo                  | Interest rate dynamics, derivatives pricing, and risk management / Lin Chen |
| Pubbl/distr/stampa      | Berlin : Springer Verlag, c1996                                             |
| ISBN                    | 3-540-60814-1                                                               |
| Descrizione fisica      | 149 p. ; 24 cm                                                              |
| Collana                 | Lecture notes in economics and mathematical systems ; 435                   |
| Disciplina              | 330.01                                                                      |
| Locazione               | MAS                                                                         |
| Collocazione            | MXX-A-143                                                                   |
| Lingua di pubblicazione | Inglese                                                                     |
| Formato                 | Materiale a stampa                                                          |
| Livello bibliografico   | Monografia                                                                  |

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| 2. Record Nr.           | UNINA9911020217903321                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| Autore                  | Lee Weng Fook                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| Titolo                  | Verilog Coding for Logic Synthesis                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| Pubbl/distr/stampa      | [Place of publication not identified], : Wiley Interscience Imprint, 2003                                                                                                                                                                                                                                                                                                                                                                                              |
| ISBN                    | 9786610556526<br>9781280556524<br>1280556528<br>9780471457558<br>0471457558<br>9780470356920<br>0470356928<br>9780471457565<br>0471457566                                                                                                                                                                                                                                                                                                                              |
| Edizione                | [1st edition]                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| Descrizione fisica      | 1 online resource (1 v.) : ill                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| Disciplina              | 621.395                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| Soggetti                | Digital electronics<br>Logic circuits - Computer-aided design<br>Verilog (Computer hardware description language)                                                                                                                                                                                                                                                                                                                                                      |
| Lingua di pubblicazione | Inglese                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| Formato                 | Materiale a stampa                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| Livello bibliografico   | Monografia                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| Note generali           | Bibliographic Level Mode of Issuance: Monograph                                                                                                                                                                                                                                                                                                                                                                                                                        |
| Nota di bibliografia    | Includes bibliographical references and index.                                                                                                                                                                                                                                                                                                                                                                                                                         |
| Nota di contenuto       | Introduction -- Asic design flow -- Verilog coding -- Coding style : best-known method for synthesis -- Design example of programmable timer -- Design example of programmable logic block for peripheral interface.                                                                                                                                                                                                                                                   |
| Sommario/riassunto      | Provides a practical approach to Verilog design and problem solving. Bulk of the book deals with practical design problems that design engineers solve on a daily basis. Includes over 90 design examples. There are 3 full scale design examples that include specification, architectural definition, micro-architectural definition, RTL coding, testbench coding and verification. Book is suitable for use as a textbook in EE departments that have VLSI courses |