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Autore	American Institute of Chemical Engineers
Titolo	1989 annual meeting. 2nd topical conference on emerging technologies in materials. Ammonia symposium. AIChE-ACS-IEC separations symposium. November 5-10,1989, San Francisco Hilton, San Francisco, CA / American Institute of Chemical Engineers.
Pubbl/distr/stampa	New York : American Institute of Chemical Engineers, 1989
Descrizione fisica	n.v. p., ill., 21 cm
Disciplina	660
Locazione	DINCH
Collocazione	04 160-53/20
Lingua di pubblicazione	Italiano
Formato	Materiale a stampa
Livello bibliografico	Monografia

2. Record Nr. UNINA9910299824003321

Autore	Pangracious Vinod
Titolo	Three-Dimensional Design Methodologies for Tree-based FPGA Architecture / / by Vinod Pangracious, Zied Marrakchi, Habib Mehrez
Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2015
ISBN	3-319-19174-8
Edizione	[1st ed. 2015.]
Descrizione fisica	1 online resource (239 p.)
Collana	Lecture Notes in Electrical Engineering, , 1876-1100 ; ; 350
Disciplina	621.39
Soggetti	Electronic circuits Electronics Microelectronics Microprocessors Circuits and Systems Electronics and Microelectronics, Instrumentation Processor Architectures
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references at the end of each chapters.
Nota di contenuto	An Overview of Three-Dimensional Integration and FPGAs -- Three-Dimensional Integration: A More Than Moore Technology -- Field Programmable Gate Arrays: An Overview -- Two Dimensional FPGAs: Configuration and CAD Flow -- Three-Dimensional FPGAs: Configuration and CAD Development -- Three-Dimensional Tree-based FPGA: Architecture Exploration Tools and Technologies -- Three-Dimensional Thermal Modeling: Tools and Methodologies -- Physical Design and Implementation of 3D Tree-based FPGAs -- Three-Dimensional FPGAs: Future Lines of Research.
Sommario/riassunto	This book focuses on the development of 3D design and implementation methodologies for Tree-based FPGA architecture. It also stresses the needs for new and augmented 3D CAD tools to support designs such as, the design for 3D, to manufacture high performance 3D integrated circuits and reconfigurable FPGA-based systems. This book was written as a text that covers the foundations of 3D integrated system design and FPGA architecture design. It was

written for the use in an elective or core course at the graduate level in field of Electrical Engineering, Computer Engineering and Doctoral Research programs. No previous background on 3D integration is required, nevertheless fundamental understanding of 2D CMOS VLSI design is required. It is assumed that reader has taken the core curriculum in Electrical Engineering or Computer Engineering, with courses like CMOS VLSI design, Digital System Design and Microelectronics Circuits being the most important. It is accessible for self-study by both senior students and professionals alike.