1.	Record Nr.	UNICAMPANIAVAN0256878
	Titolo	1 / Ordinamento istituzionale
	Pubbl/distr/stampa	Sant'Arcangelo di Romagna, : Maggioli, 2023
	Descrizione fisica	XI, 1166 p. ; 24 cm
	Disciplina	658.8
	Lingua di pubblicazione	Italiano
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
2.	Record Nr.	UNINA9910437890703321
	Autore	Wang Dong
	Titolo	Robust Filtering and Fault Detection of Switched Delay Systems / / by Dong Wang, Peng Shi, Wei Wang
	Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2013
	ISBN	3-642-37685-1
	Edizione	[1st ed. 2013.]
	Descrizione fisica	1 online resource (XIV, 148 p. 31 illus.)
	Collana	Lecture Notes in Control and Information Sciences, , 0170-8643 ; ; 445
	Disciplina	629.8312
	Soggetti	Control engineering
		System theory
		Control and Systems Theory Systems Theory Control
	Lingua di pubblicazione	
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
	Note generali	Bibliographic Level Mode of Issuance: Monograph
	Nota di contenuto	Part I Filter Design Part II Fault Detection Filter Design Part II Fault Detection.
	Sommario/riassunto	Switched delay systems appear in a wide field of applications including networked control systems, power systems, memristive systems. Though the large amount of ideas with respect to such systems have

generated, until now, it still lacks a framework to focus on filter design and fault detection issues which are relevant to life safety and property loss. Beginning with the comprehensive coverage of the new developments in the analysis and control synthesis for switched delay systems, the monograph not only provides a systematic approach to designing the filter and detecting the fault of switched delay systems, but it also covers the model reduction issues. Specific topics covered include: (1) Arbitrary switching signal where delay-independent and delay-dependent conditions are presented by proposing a linearization technique. (2) Average dwell time where a weighted Lyapunov function is come up with dealing with filter design and fault detection issues beside taking model reduction problems. The monograph is intended for academic researchers and engineers in systems and control community who will discover of particular value in dealing with filter design and fault detection of switched system and time delay systems. In addition, it will be helpful and complementary reading for graduate students in such field.